

REMARKS

Claims 1-17, 21-27 and 31-36 have been canceled. Thus, Claims 18-20 and 28-30 are currently pending in the present application, of which Claim 28 has been amended.

The word "said" has been removed from Claim 28, as suggested by the Examiner. Thus, the claimed objection is believed to be overcome.

Rejection under 35 U.S.C. § 102

Claims 18-20, 24-29 and 31-36 were rejected under 35 U.S.C. § 102(b) as being anticipated by *Requa et al.* (The Piecewise Data Flow Architecture: Architectural Concepts, IEEE transactions on Computers, May 1993). Applicants respectfully traverse such rejection.

Claim 1 (and similarly Claim 28) recites a step of "assigning each of said instruction groups to a respective set of said interconnected computation nodes such that results yielded by an instruction within one of said instruction groups are sent directly to an instruction within another one of said instruction groups or an output operand associated with an instruction within one of said instruction groups is directly written to a register file to be read by an instruction within another one of said instruction groups as an input operand."

On page 3 of the Final Office Action, the Examiner asserts that the claimed assigned step is disclosed by *Requa* on the second column of page 427 and on page 428. According to *Requa* on page 427, when "a scalar instruction has all such dependencies to be satisfied, it can be sent to any scalar unit for processing. The mechanism that sits between the instruction registers and the scalar processors is a FIFO queue. Each producer (instruction register) has an input port into the FIFO..." Since *Requa*'s producer is connected to a FIFO queue, that means results from the producer are not "sent directly to an instruction within another one of said instruction groups" (emphasis added), as claimed.

Because the claimed invention recites novel features that are not taught or suggested by the cited references, the § 102 rejection is believed to be overcome.

CONCLUSION

Claims 18-20 and 28-30 are currently pending in the present application. For the reasons stated above, Applicants believe that independent Claims 18 and 28 along with their respective dependent claims are in condition for allowance. The remaining prior art cited by the Examiner, but not relied upon, has been reviewed and is not believed to show or suggest the claimed invention.

Please charge \$60 against Dillon & Yudell Deposit Account No. **50-3083** for the one-month extension. No additional fee or extension of time is believed to be necessary; however, in the event that any additional fee or extension of time is required for the prosecution of this application, please charge it against Dillon & Yudell Deposit Account No. **50-3083**.

Respectfully submitted,



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